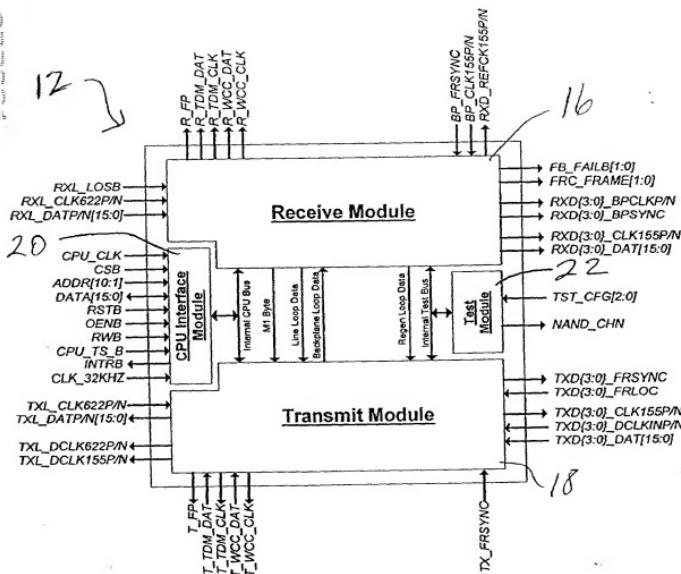


Figure 1



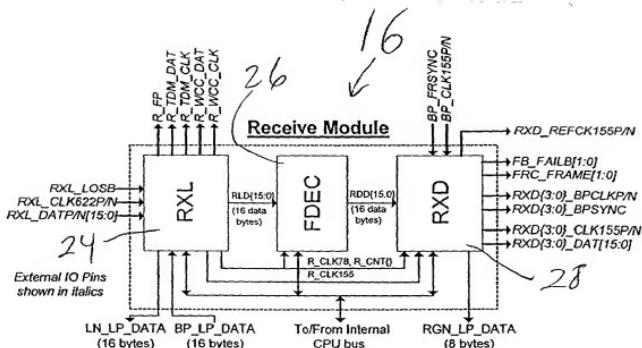


Fig. 3

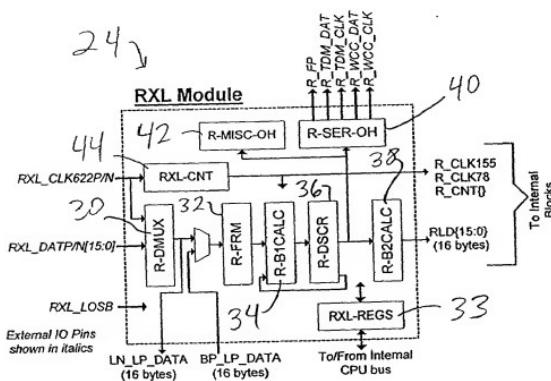


Fig. 4

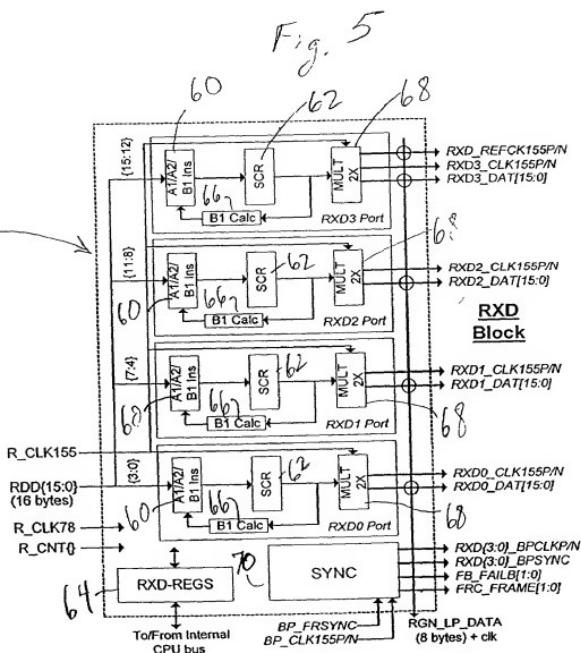
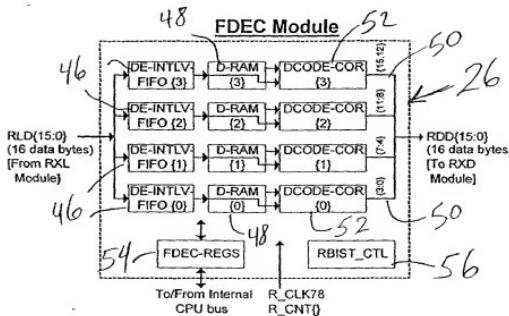


Fig. 6

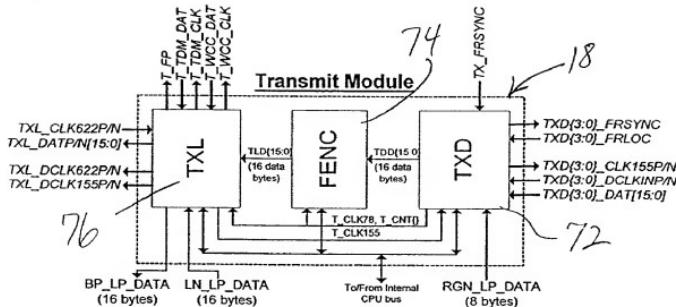


Fig. 7

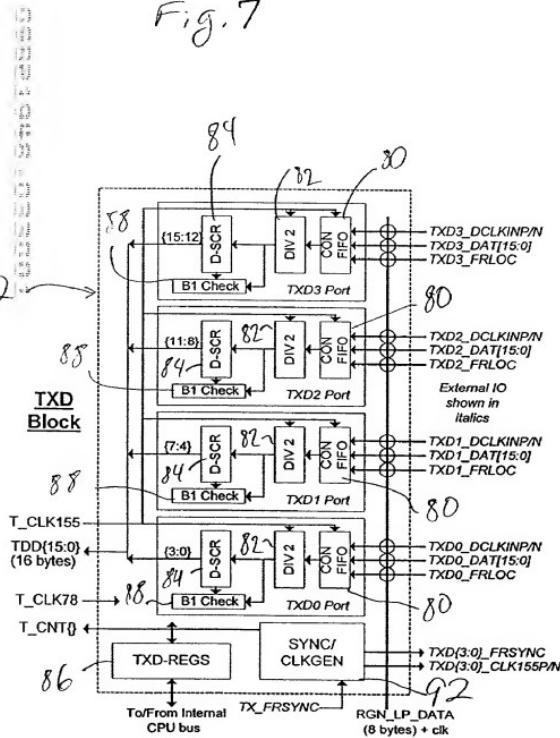


Fig. 8

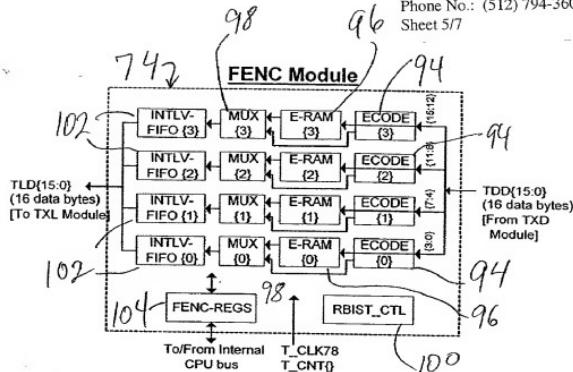


Fig. 9

External IO Pins shown in italics

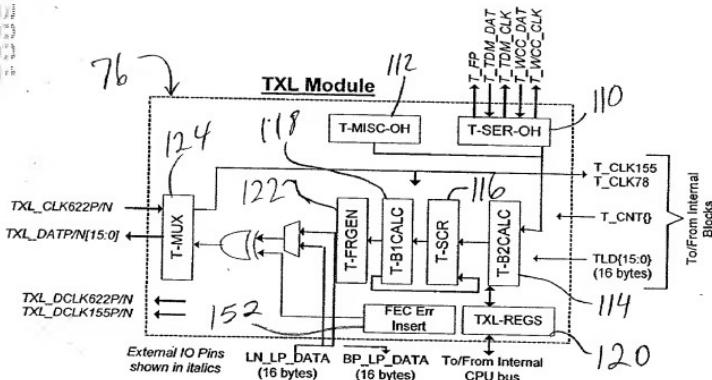


Fig. 10

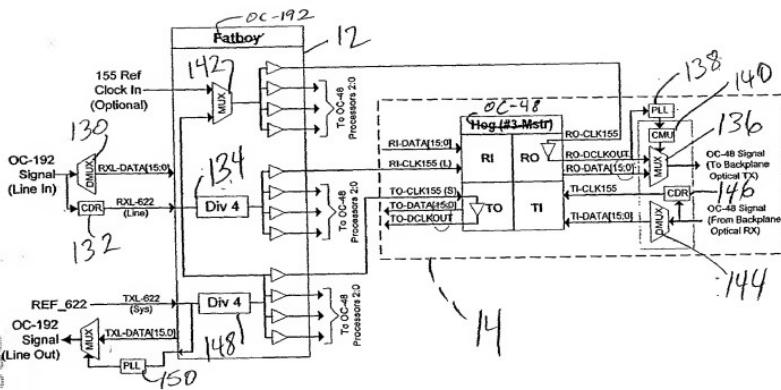


Fig. 11

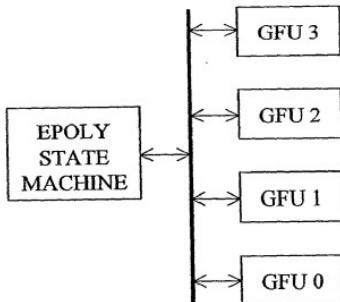


Figure 12

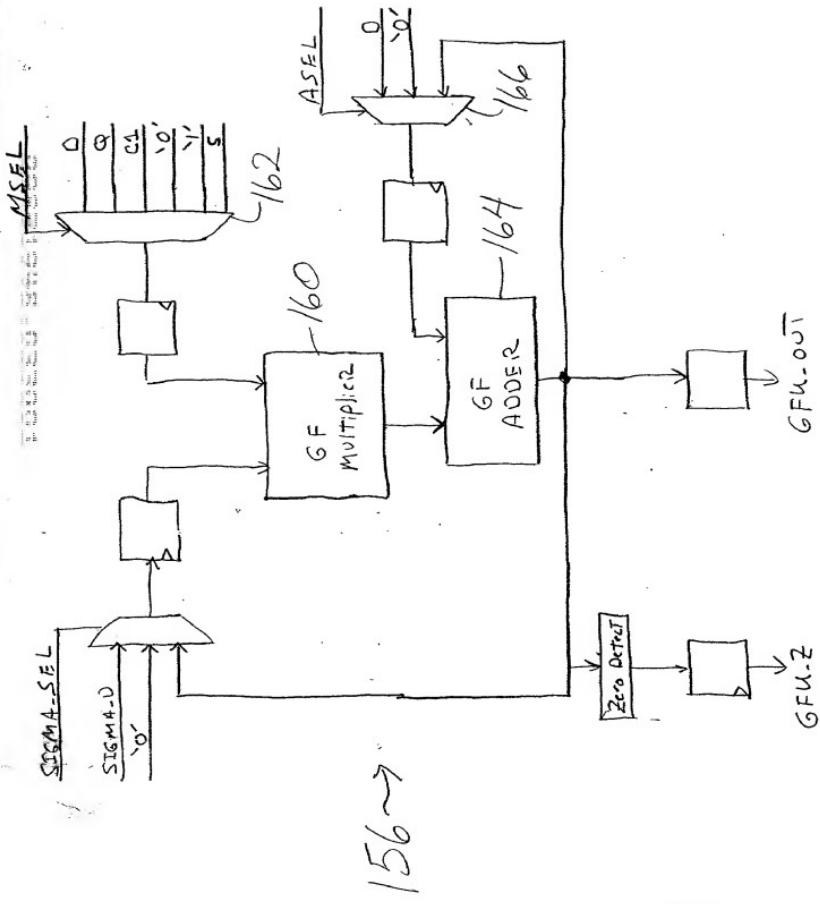


Fig. 13